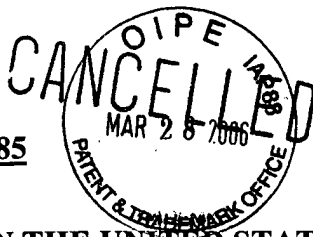


S/N 10/643,585



PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Steven L. Scott	Examiner:	Sheng Jen Tsai
Serial No.:	10/643,585	Group Art Unit:	2186
Filed:	August 18, 2003	Docket:	1376.700US1
Title:	LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER		

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. § 1.97(c)(2), Applicants have included the fee of \$180.00 as set forth in 37 C.F.R. § 1.17(p). Please charge any additional fees or credit any overpayment to Deposit Account No. 19-0743.

03/29/2006 NNGUYEN1 00000057 190743 10643585

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~~01 FC:1806~~

~~180.00 OP~~

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Serial No :10/643,585

Filing Date: August 18, 2003

Title: LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER

Page 2

Dkt: 1376.700US1

Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications are no longer required to be provided to the Office. Notification of this change was provided in the United States Patent and Trademark Office OG Notices dated October 12, 2004. Thus, Applicant has not included copies of any U.S. Patents or Published Applications cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

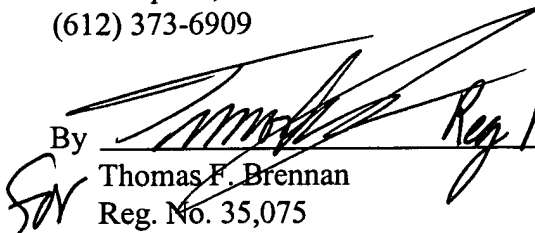
Respectfully submitted,

STEVEN L. SCOTT

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
P.O. Box 2938  
Minneapolis, MN 55402  
(612) 373-6909

Date 27 March '06

By  Reg. No. 40,957  
SAR Thomas F. Brennan  
Reg. No. 35,075

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 27 day of March, 2006.

CANDIS BUENDING

Name

Signature 

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/643,585
Filing Date	August 18, 2003
First Named Inventor	Scott, Steven
Group Art Unit	2186
Examiner Name	Tsai, Sheng

Attorney Docket No: 1376.700US1

**US PATENT DOCUMENTS**

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-2003/0005380A1	01/02/2003	Nguyen, H. T., et al.	06/29/2001
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**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		"Cray Assembly Language (CAL) for Cray X1™ Systems Reference Manual", Section 2.6, Memory Ordering, <a href="http://docs.cray.com/books/S-2314-51/index.html">http://docs.cray.com/books/S-2314-51/index.html</a> , (June, 2003), 302 pgs.	
		"ECPE 4504: Computer Organization Lecture 12: Computer Arithmetic", The Bradley Department of Electrical Engineering, (October 17, 2000), 12 pgs.	
		"Msync - Synchronise Memory with Physical Storage", The Single UNIX® Specification, Version 2: Msync, The Open Group, <a href="http://www.opengroup.org/onlinepubs/007908799/xsh/msync.html">http://www.opengroup.org/onlinepubs/007908799/xsh/msync.html</a> , (1997), 3 pgs.	
		ABTS, D., "So Many States, So Little Time: Verifying Memory Coherence in the Cray X1", Parallel and Distributed Processing Symposium, (April 22, 2003), 11-20	
		COHOON, JAMES, et al., "C++ Program Design", McGraw-Hill Companies Inc. 2nd Edition, (1999), page 493	
		ERNST, D., et al., "Cyclone: A Broadcast-Free Dynamic Instruction Scheduler with Selective Replay", 30th Annual International Symposium on Computer Architecture (ISCA-2003), (June, 2003), 10 pgs.	
		PATTERSON, D. A., et al., Computer Architecture: A Quantitative Approach, 2nd Edition, Morgan Kaufmann Publishers, Inc., San Francisco, CA, (1996), 39-41	

**EXAMINER****DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional) <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached

Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>	Complete if Known <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 40%;"><b>Application Number</b></td> <td>10/643,585</td> </tr> <tr> <td><b>Filing Date</b></td> <td>August 18, 2003</td> </tr> <tr> <td><b>First Named Inventor</b></td> <td>Scott, Steven</td> </tr> <tr> <td><b>Group Art Unit</b></td> <td>2186</td> </tr> <tr> <td><b>Examiner Name</b></td> <td>Tsai, Sheng</td> </tr> </table>	<b>Application Number</b>	10/643,585	<b>Filing Date</b>	August 18, 2003	<b>First Named Inventor</b>	Scott, Steven	<b>Group Art Unit</b>	2186	<b>Examiner Name</b>	Tsai, Sheng
<b>Application Number</b>	10/643,585										
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<b>First Named Inventor</b>	Scott, Steven										
<b>Group Art Unit</b>	2186										
<b>Examiner Name</b>	Tsai, Sheng										
Sheet 2 of 2	Attorney Docket No: 1376.700US1										

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		PATTERSON, D. A., et al., <u>Computer Architecture: A Quantitative Approach</u> , 2nd Edition, Morgan Kaufmann Publishers, Inc., San Francisco, CA, (1996), 699-708	
		PATTERSON, D. A., et al., <u>Computer Architecture: A Quantitative Approach</u> , 2nd Edition, Morgan Kaufmann Publishers, Inc., San Francisco, CA, (1996), 179-187, 373-384	
		PATTERSON, D. A., et al., <u>Computer Architecture: A Quantitative Approach</u> , 2nd Edition, Morgan Kaufmann Publishers, Inc., San Francisco, CA, (1996), 194-197	

**EXAMINER**

**DATE CONSIDERED**